

Abstracts

High-Power Frequency Multiplier Using MIS Varactors (Short Papers)

F. Schumacher. "High-Power Frequency Multiplier Using MIS Varactors (Short Papers)." 1973 Transactions on Microwave Theory and Techniques 21.10 (Oct. 1973 [T-MTT]): 648-649.

The experiment described demonstrates the applicability of MIS varactors in microwave power circuits. A frequency doubler with 55-percent overall efficiency and pulsed output power of 5.5 W at 5.4 GHz with a duty cycle of 50 percent has been built by using two MIS varactors in parallel. The multiplier has a coaxial low-pass filter at the input and a waveguide output, allowing a 3-dB bandwidth of 8 percent.

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